



Attorney's Docket No.: 07977/204002/US3480D1

2816
#8
Supplemental
IDS
018 C1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : H. Ohtani
Serial No.: 09/389,393
Filed : September 3, 1999
Title : SEMICONDUCTOR DEVICE HAVING INSULATED GATE ELECTRODE

Art Unit : 2816
Examiner : B. William Baumeister

Commissioner for Patents
Washington, D.C. 20231

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Sir:

Further to and as specified in the Information Disclosure Statement filed October 16, 2000, Applicant has prepared a full translation of the enclosed cited Japanese Document No. 06-250214. Kindly accept this document and Information Disclosure Statement supplemental to the previously filed IDS. No fee is required.

Consideration of the foregoing and enclosure plus the return of a copy of the enclosed Form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

NO 1449 INCLUDED.
CITED ON 892 of Paper # 12.
bwb 6/30/01

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

December 4, 2000

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Susan Regan

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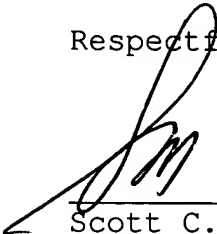
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Respectfully submitted,

Date: 12/4/00



Scott C. Harris
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